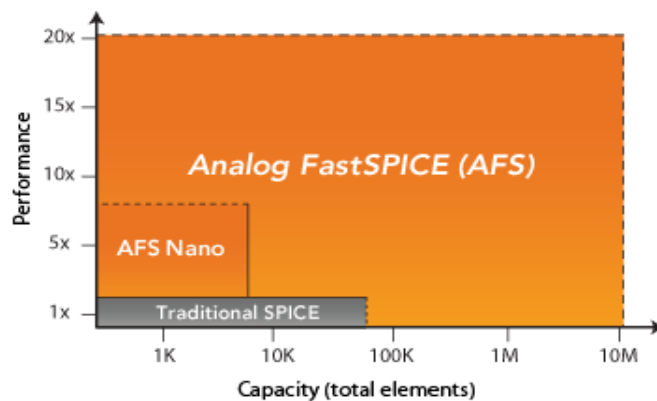




Analog FastSPICE Nano SPICE Simulator

AFS Nano™ is the industry's fastest SPICE simulator for block-level IC design. Part of the *Analog FastSPICE* unified circuit verification platform, *AFS Nano* delivers foundry-certified true SPICE accuracy, 5x-10x faster for blocks with up to 5K elements for only \$1,900 (1-yr license).

Analog FastSPICE Circuit Simulation with True SPICE Accuracy



>2x Faster Block-Level Design

AFS Nano enables IC designers to design and rigorously characterize their blocks at least 2x faster and for a fraction of the cost of leading SPICE simulators. *AFS Nano* verifies blocks with up to 5K elements, delivering foundry-certified true SPICE accurate waveforms 5x-10x faster for even moderately sized blocks (e.g., >1K elements and >1-hr runtime).

Analog FastSPICE Platform

AFS Nano is part of the *AFS Platform*—the industry's only unified verification platform for analog, mixed-signal, and RF design. The single executable provides true SPICE accuracy, 5x-20x higher performance than traditional SPICE, and >10 million element capacity. Design teams using the *AFS Platform* realize 2x higher efficiency than with traditional simulators which trade off accuracy, performance, capacity, and functionality.

Analyses

- DC (.op and sweep), AC, transient
- Network analysis, transfer function
- Monte Carlo, alter, sweep, .measure
- AC noise analysis (.noise)
- Up to 5K element capacity

Specifications

- Inputs
 - Leading SPICE netlists and models
- Model support
 - Gummel-Poon, HICUM, Mextram, VBIC
 - BSIM3, BSIM4, BSIMSOI, MOS11, PSP
 - MOS1, MOS3, JFET, Diode, Juncap
 - Verilog-A, S-parameter, W-element
- Outputs
 - PSF ASCII, PSF binary, tr0, FSDB
 - Nutmeg ASCII, Nutbin, Nutbinf
- Integration
 - Leading custom design environment
- Operating Systems
 - Linux or Solaris

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